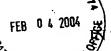
PATENT APPLICATION



THE UNITED STATES PATENT AND TRADEMARK OFFICE

plication of

Shin FUJITA

Application No.:

10/629,590

Docket No.: 116725

Filed: July 30, 2003

For:

TIMING ADJUSTMENT CIRCUIT, DRIVE CIRCUIT, ELECTROOPTIC DEVICE

AND ELECTRONIC EQUIPMENT

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

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Respectfully, submitted,

James A. Oliff

Registration No. 27,075

Joel S. Armstrong

Registration No. 36,430

JAO:JSA/hjr

Date: February 4, 2004

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400

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DRAWINGS

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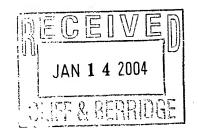
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CONFIRMATION NO. 6224

UPDATED FILING RECEIPT



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Applicant(s)

Shin Fujita, Chino-shi, JAPAN;

Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

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Non-Publication Request: No

Early Publication Request: No

Title

Timing adjustment circuit, drive circuit, electrooptic device and electronic equipment

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